

Sheet 1 of 1

**INFORMATION DISCLOSURE CITATION
IN AN APPLICATION**

(Use several sheets if necessary)

Attorney Docket No.:
8013-1257

Application No.
10/56119
NEW NATIONAL PHASE

Applicant:
Takashi HARADA et al.

Filing Date:
December 16, 2005

Group Art Unit:
2825

U.S. PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Name	Class	Subclass	Filing date (if appropriate)

FOREIGN PATENT DOCUMENTS

Examiner Initial	Document Number	Date	Country	Class	Subclass	Translation	
						Yes	No
M	2000-242674	9/8/2000	JAPAN - ABSTRACT			X	

OTHER DOCUMENTS (Including Author, Title, Date, Pertinent Pages, Etc.)

A. Kamo et al., An Optimization Method for Placement of Decoupling Capacitors on Printed Circuit Board, Proceedings of the IEEE Conference on Electrical Performance of Electronic Packaging, pages 73 to 76, 23 October, 2000 (23.10.00), IEEE, USA, p76 IV. Conclusion

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* English language abstract provided for the Examiner's convenience

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